

A2 Architecture Test 2

Your Name:

1. Explain what is meant by a Register.
2. Explain what is meant by a Flag.
3. In which register are the Flags found?
4. Explain what is meant by the Program Counter.
5. Explain what is meant by the Working Register.
6. Explain what is meant by the stack.
7. Explain how the stack is organised.
8. In which register are Stack operations organised?
9. Explain the purpose of the Clock.
10. Explain the purpose of TRISA.
11. What is meant by RISC and CISC?
12. How does the Harvard architecture differ from the conventional Von Neumann machine?

13. Explain the operation of an interrupt. Which registers are altered? How is the memory altered?
14. Explain what is meant by a subroutine.
15. The JPZ operation reads one register and writes to another. Explain.
16. Describe the register and memory changes made by the CALL operation.
17. Describe the register and memory changes made by the RET operation.
18. Describe the register changes made by the JMP operation.
19. What is meant by Polling?
20. What is the Timer (TMR) register used for. Explain its operation.
21. What is the Prescaler (PRE) register used for. Explain its operation.
22. What is the purpose of the ASSEMBLER program?